

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE  
STATEMENTDocket Number:  
2885/31Application Number  
09/598,926Filing Date  
June 21, 2000Examiner  
G. LamarreArt Unit  
2133Invention Title  
METHOD OF REPAIRING INTEGRATED CIRCUITSInventor(s)  
Martin VORBACH et al.

BY HAND DELIVERY TO:  
Examiner Guy Lamarre, Art Unit 2133  
U.S. Patent and Trademark Office  
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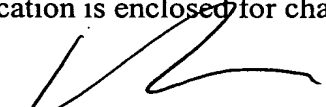
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Technology Center 2100

- I. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the references listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
- II. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
- III. It is believed that no fees are due in connection with this Information Disclosure Statement, as these references were all **previously submitted** to the PTO with the IDS filed by Applicants on February 26, 2001. However, should any fees be due, the Commissioner is authorized to charge Deposit Account Number 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated: 30 Sept 2003

By:

  
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